Memo/Reply From
JOSHUA LEDERBERG

TO: Bob Noyce

JUL 31 1981

VAPX-432

The literature I got was very exciting, though some of it, predictably, is over my head. The level of explication in what I saw made it hard for me to tell whether the local processors had enough cache memory (or enough general registers) to minimize contention on the bus when you hang a significant number (think of 10^2....) processors on it.

But thank you.

I certainly enjoyed our conversation; and whether we can conveniently rejoin in SFO on 8/17, I hope we interact again soon.

Yours,

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